

<b>L Number</b>	<b>Hits</b>	<b>Search Text</b>	<b>DB</b>	<b>Time stamp</b>
<b>1</b>	<b>2485</b>	<b>((memory storage) near3 (module bank sector) and ((cache storage memory) with tag)</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT</b>	<b>2003/09/04 16:27</b>
<b>2</b>	<b>371</b>	<b>((storage cache memory) adj4 (controller logic)) and way and ((evict\$3 victim) near3 (line entry buffer queue register))</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT</b>	<b>2003/09/04 16:26</b>
<b>3</b>	<b>120</b>	<b>((memory storage) near3 (module bank sector) and ((cache storage memory) with tag)) and (((storage cache memory) adj4 (controller logic)) and way and ((evict\$3 victim) near3 (line entry buffer queue register)))</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT</b>	<b>2003/09/04 16:29</b>
<b>4</b>	<b>106</b>	<b>711/\$.ccis. and (((memory storage) near3 (module bank sector) and ((cache storage memory) with tag)) and (((storage cache memory) adj4 (controller logic)) and way and ((evict\$3 victim) near3 (line entry buffer queue register))))</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT</b>	<b>2003/09/04 16:29</b>
<b>5</b>	<b>946</b>	<b>(memory storage) near3 (module bank sector) and ((cache storage memory) with tag) and ((stor\$3 writ\$3) adj3 back)</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT</b>	<b>2003/09/04 16:28</b>
<b>6</b>	<b>102</b>	<b>(((storage cache memory) adj4 (controller logic)) and way and ((evict\$3 victim) near3 (line entry buffer queue register))) and ((memory storage) near3 (module bank sector) and ((cache storage memory) with tag) and ((stor\$3 writ\$3) adj3 back))</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT</b>	<b>2003/09/04 16:29</b>

<b>L Number</b>	<b>Hits</b>	<b>Search Text</b>	<b>DB</b>	<b>Time stamp</b>
8	7366	((memory cache storage) adj4 (controller logic circuit)) same (cache memory storage) near4 (index tag field)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/04 16:53
9	144	((memory storage) near3 (bank module sector block)) and ((victim eviction) near4 (line entry cache buffer queue register)) and ((memory storage) near3 (module bank sector) and ((cache storage memory) with tag))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/04 17:05
10	33	(decoder deserializer) and (((memory storage) near3 (bank module sector block)) and ((victim eviction) near4 (line entry cache buffer queue register)) and ((memory storage) near3 (module bank sector) and ((cache storage memory) with tag)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/04 17:05
11	452	(sequencer serializer) and (((memory cache storage) adj4 (controller logic circuit)) same (cache memory storage) near4 (index tag field))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/04 17:04
12	322	(decoder deserializer) and ((sequencer serializer) and (((memory cache storage) adj4 (controller logic circuit)) same (cache memory storage) near4 (index tag field))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/04 17:05
13	374	((memory storage) near3 (bank module sector block)) and ((sequencer serializer) and (((memory cache storage) adj4 (controller logic circuit)) same (cache memory storage) near4 (index tag field))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/04 17:06
14	255	((memory storage) near3 (bank module sector block)) and ((decoder deserializer) and ((sequencer serializer) and (((memory cache storage) adj4 (controller logic circuit)) same (cache memory storage) near4 (index tag field))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/04 17:06

<b>L Number</b>	<b>Hits</b>	<b>Search Text</b>	<b>DB</b>	<b>Time stamp</b>
<b>1</b>	<b>49</b>	<b>((system memory storage) adj3 (controller logic circuit)) with ((tag index field) adj3 (buffer array register))) same ((memory storage) adj3 (bank module sector block))</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT</b>	<b>2003/09/04 19:54</b>